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APPLICATION NO.	FI	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
09/502,791	02/11/2000		Yoshinobu Nakamura	1046.1210/JDH	5681
21171	7590	02/25/2004	EXAMINER		INER
STAAS & HALSEY LLP				SHRADER, LAWRENCE J	
SUITE 700 1201 NEW YORK AVENUE, N.W. WASHINGTON, DC 20005				ART UNIT	PAPER NUMBER
				2124	- 11
			DATE MAILED: 02/25/2004		

Please find below and/or attached an Office communication concerning this application or proceeding.

	Annlicont(c)					
Application No.	Applicant(s)					
09/502,791	NAKAMURA, YOSHINOBU					
Examiner	Art Unit					
Lawrence Shrader	2124					
ears on the cover sheet with the c	correspondence address					
IS SET TO EXPIRE 3 MONTH(6(a). In no event, however, may a reply be time within the statutory minimum of thirty (30) day ill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE date of this communication, even if timely filed	nely filed /s will be considered timely. I the mailing date of this communication. ED (35 U.S.C. § 133).					
<u>/2003</u> .						
action is non-final.						
Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
application. In from consideration. and/or election requirement. Expressed or b) objected to by the drawing(s) be held in abeyance. See						
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priority under 35 U.S.C. § 119(as have been received. shave been received in Applicatity documents have been received (PCT Rule 17.2(a)). Of the certified copies not received	ion No ed in this National Stage					
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DETAILED ACTION

- 1. This action is in response to the amendment filed December 8, 2003.
- 2. Claims 1-10, and 14-15 remain rejected under 35 USC § 103 and repeated below; and claims 11-13, and 16 remain subject to restriction.

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claim1 7; 8 10; 14 and 15 are rejected under 35 U.S.C. 103(a) as being unpatentable over Nakamura, Japanese Patent No. JP405100864A in view of Gan et al., U.S. Patent 5,878,238 (hereinafter referred to as Gan).

In regard to claim 1:

"A program processing unit judging decoding the program, and judging whether or not an address described in the decoded program is effective...;" Nakamura discloses a method whereby a judgment (or a decision) is made concerning the conversion of a label address during the processing of a program (Abstract).

"A label address translating unit performing an exception handling for re-writing an address ..." Nakamura discloses a method whereby a judgment (or a decision) is made

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concerning the conversion of a label address during the processing of a program (Abstract). If a label is encountered during the code assembly the program, a table look-up is implemented to obtain and write a physical address. The use of an exception is not explicitly specified in Nakamura. However, Gan teaches the use of an exception handler to look up an address in a table. Therefore, it would have been obvious to one skilled in the art at the time the invention was made to modify the Nakamura invention (judging the content of an address label, implementing an address table look-up, and writing the address) with an exception handler as taught by Gan to lookup and write the address when judged necessary by the Nakamura invention. This modification would be obvious because one of ordinary skill would recognize the exception can be thrown when it is judged that the address is not obtained from the label and the lookup/writing process is then handled by the dedicated exception function in order improve processor performance by efficiently providing the effective address.

In regard to claim 2, incorporating the rejection of claim 1:

"...said label address translating unit uses a table showing a relationship between the label and effective address." Nakamura discloses a table, which provides the relationship between the label and the effective address (Abstract).

In regard to claim 3, incorporating the rejection of claim 1:

"...said program processing unit...judges that the address is not effective." Nakamura teaches that a decision unit determines (judges) that a label must be converted to a physical address (Abstract).

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In regard to claims 4-6 (the method), rejected for the same reasons put forth in the rejection of claims 1-3 (the device).

In regard to claim 7 (storage medium), rejected for the same reasons put forth in the rejection of claim 1 (the device) above.

In regard to claim 8:

Rejected for the same reasons put forth in the rejection of claim 1. The same exception process could be implemented whether the target program is interpreted or compiled.

In regard to claim 9 (a method), rejected for the same reasons put forth in the rejection of claim 8 (a device).

In regard to claim 10 (computer medium), rejected for the same reasons put forth in the rejection of claim 8 (a device).

In regard to claim 14:

Rejected for the same reasons put forth in the rejection of claim 1. The same judging and exception handling for writing an address could be implemented while the target program is executed and the addresses dynamically assigned.

In regard to claim 15:

Rejected for the same reasons put forth in the rejection of claim 1. The same judging and exception handling for writing an address could be implemented if the target program is processed either by an interpreter or by a compiler rather than a dynamic configuration as in claim 14.

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Response to Amendment

5. Applicant's arguments filed on December 8, 2003 have been fully considered but they are not persuasive:

Applicant has argued:

As acknowledged by the Examiner, Nakamura does not teach exception handling. Nevertheless, Gan et al. is cited for teaching same. However, Gan et al. relates to when an incompatible or "semi-compliant" video card is used with a PC, i.e., is put in the wrong slot. Instead of the video card being totally unusable, the PC detects the incompatible video card (see, e.g., Col. 2, lines 66-67 and Col. 4, lines 1-16), and any attempted write cycle to an address in the VGA address space results in the execution of an "exception handler".

More particularly, the option ROM of the video card is activated and the I/O address space thereof is read. An address look up table is also generated. A determination is then made as to whether the address of the write cycle is part of the address space of the card as specified in the look up table. If not, the bridge is remapped to handle the address. The cycle is forwarded to the video card, the card is initialized and the bridge is remapped to its original mapping. A return from this exception handling is then executed. In this way, the video card may be initialized and used to instruct the user, via a displayed message, only that the video card should be moved to another slot, before the PC can be used further. See Col. 4, lines 14 – 51.

Thus at most, when an incorrectly installed video card is detected, Gan et al. teaches going to a pre-set group of addresses, and an appropriate address is selected and executed to allow the video card to operate to a limited extent to advise the user that the card must be moved to a more appropriate slot in order to properly function.

Gan et al. does not, like Nakamura does not, teach the above-described recited judgment of whether the address is effective and, if ineffective, re-writing the label as an effective address directly into the program.

Examiner's response:

The Office Action shows that Nakamura teaches the converting or decoding of a program and rewriting an address described in the decoded program into an effective address. The

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exception handling part of the applicant's claim is not a unique feature as shown by the Gan reference. Exceptions can be used for many functions. The rewriting of the address could be done several ways, including an interrupt or exception.

Conclusion

6. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Lawrence Shrader whose telephone number is (703) 305-8046.

The examiner can normally be reached on M-F 08:00-16:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kakali Chaki can be reached on (703) 305-9662. The fax phone numbers for the

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organization where this application or proceeding is assigned are (703) 746-7239 for regular communications and (703) 746-7238 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3900.

Lawrence Shrader Examiner Art Unit 2124

February 17, 2004

TODO MGBERG PRIMARY EXAMINER